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FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	
<b>LIST OF REFERENCES CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		ATTY. DOCKET NO.	SERIAL NO.
		58268.00366	New Continuation
		APPLICANT Dennis Sungik LEE et al.	
FILING DATE	GROUP		
January 6, 2003	2175		

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EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
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	AB 5,644,784	Jul. 1997	Peek			
	AD 5,828,653	Oct. 1998	Goss			
	AE 5,423,015	Jun. 1995	Chung			
	AF 5,696,899	Dec. 1997	Kalwitz			
	AG 6,061,351	May 2000	Erimli et al.			
	AH 5,748,631	May 1998	Bergantino et al.			
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CH	WO 99/00939	Jan. 1999	WIPO					
CI	WO 99/00938	Jan. 1999	WIPO					
CJ	WO 99/00944	Jan. 1999	WIPO					
CK	WO 99/00945	Jan. 1999	WIPO					
CL	EP0849917A2	Jun. 1998	EPO					
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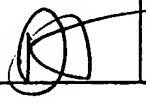
CN	"A High-Speed CMOS Circuit for 1.2-Gb/s 16 x 16 ATM Switching," Alain Chemarin et al. 8107 IEEE Journal of Solid-State Circuits 27(1992) July, No. 7, New York, US, pages 1116-1120
CO	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
CP	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
CQ	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
CR	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
CS	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2- Sec. 5.3, pages 309-320
CT	Kenneth Schultz, et al., "Architectures for Large-Capacity CAMs," Integration, the VLSI Journal, North Holland Publishing Company, Amsterdam, The Netherlands, vol. 18, no. 2, 1 June 1995
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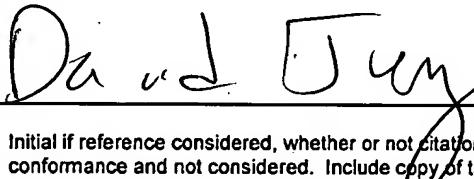
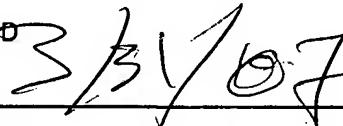
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	CV	Pao et al., "Efficient Hardware Architecture for Fast IP Address Lookup," INFOCOM 2002, Twenty-First Annual Joint Conference of the IEEE Computer and Communication Societies, Proceedings, IEEE Vol. 2, 23-27 June 2002, pp 555-561, vol. 2
	CW	Elm et al., "Fault Detection and Fault Localization Using IDDQ-Testing in Parallel Testable FAST-SRAMs," VLSI Test Symposium, 1994, Proceedings, 12th IEEE, 25-28 April 1994, pp 380-385

EXAMINER		DATE CONSIDERED	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			